

ABSTRACT OF THE DISCLOSURE

A microprocessor which can shorten a testing period and facilitate the production of a test program is provided. The microprocessor of the present invention includes a CPU which performs certain arithmetic operations, an address bus connected to the CPU, a circuit unit, such as comparators, which utilizes an address on the address bus. In an function test of the circuit unit, a test circuit, instead of the CPU, generates a test address for testing the circuit unit through the address bus. Also, a special instruction is issued to transmit an address through the bus so as to prevent the CPU from overrunning